

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): W. Rhee et al.
Docket No.: YOR920030258US1
Serial No.: 10/697,751
Filing Date: October 30, 2003
Group: 2816
Examiner: Linh M. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: William E. Lewis Date: March 8, 2005

Title: Voltage-Controlled Delay Circuit Using
Second-Order Phase Interpolation

TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following document relating to the above-identified patent application:

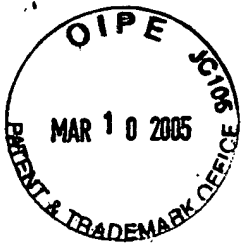
- (1) Response to Office Action with enclosure(s).

Please extend the period for response by one month to March 8, 2005. Please charge **Ryan, Mason & Lewis Deposit Account No. 50-0762** the amount of \$120, to cover this fee. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Date: March 8, 2005

Respectfully submitted,

William E. Lewis
William E. Lewis
Attorney for Applicant(s)
Reg. No. 39,274
Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, NY 11560
(516) 759-2946



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Signature: *V. Benvenuti* Date: March 8, 2005

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RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the outstanding Office Action dated November 8, 2004, Applicants present the following remarks for consideration.

03/11/2005 BABRAHA1 00000028 500762 10697751

01 FC:1251 120.00 DA